

# EUROPEAN PATENT OFFICE

## Patent Abstracts of Japan

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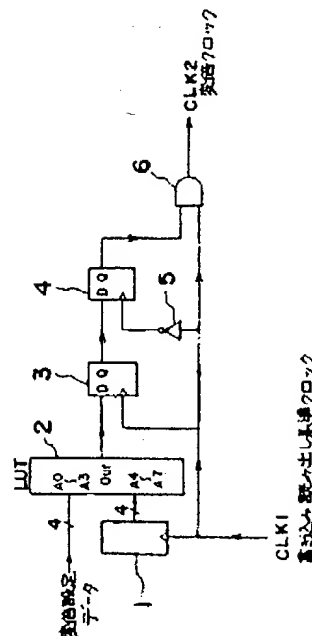
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TITLE : CLOCK CONTROLLER



**ABSTRACT :** **PURPOSE:** To make moire fringes less easily recognizable by writing a pattern of a magnification clock in response to a magnification onto a lookup table and reading the said clock synchronously with a reference clock so as to change the pattern of the magnification clock if moire takes place on a printer or the like.

**CONSTITUTION:** The output of a counter 1 is fed to addresses A4~A7 of the lookup table (LUT) 2 and a magnification setting data is fed to addresses A0~A3 of the LUT 2. An output of a D F/F 4 is fed to an AND gate 6 together with a reference clock CLK 1 to obtain a magnification clock CLK 2. A magnification data is designated to the LUT 2 in response to the addresses A0~A3, the degree of inhibiting the output of the reference clock CLK 1 is changed sequentially in response to the magnification setting data and its inhibition cycle is uniformized. In changing the content of the LUT 2, the generated pattern of the magnification clock CLK 2 is changed easily, and if a moire takes place in the printed output picture, it is relieved by changing the generation pattern of the magnification clock CLK 2.

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